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1 **Group testing with DNA chips: generating designs and decoding experiments**
Schliep, A.; Torney, D.C.; Rahmann, S.;
Bioinformatics Conference, 2003. CSB 2003. Proceedings of the 2003 IEEE , 11-14 Aug. 2003
Pages:84 - 91

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2 **Conference Record of Thirty-Fifth Asilomar Conference on Signals, Systems and Computers (Cat.No.01CH37256)**

Signals, Systems and Computers, 2001. Conference Record of the Thirty-Fifth Asilomar Conference on , Volume: 1 , 4-7 Nov. 2001
[Abstract] [PDF Full-Text (265 KB)] **IEEE CNF**

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3 **System level design, a VHDL based approach**
van den Hurk, J.; Dilling, E.;
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4 **High performance, high throughput turbo/SOVA decoder design**
Zhongfeng Wang; Parhi, K.K.;
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Pages:570 - 579

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5 **Effect of transition noise on turbo decoding for optical data storage**
Song, H.; Vijaya Kumar, B.V.K.;
Optical Data Storage, 2000. Conference Digest , 14-17 May 2000

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6 **Probabilistic application modeling for system-level performance analysis**
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10 **LZW-based code compression for VLIW embedded systems**
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11 **Design methods for system-on-a-chip control codecs to enhance performance and reuse**
Fisher, J.S.; Murphy, E.J., III; Bibyk, S.B.;
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12 **Microarchitecture support for improving the performance of load target prediction**

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14 **The decodability of multiple-PRF radar waveforms**
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Pages:544 - 547

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15 **Class-based decompressor design for compressed instruction memory in embedded processors**
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